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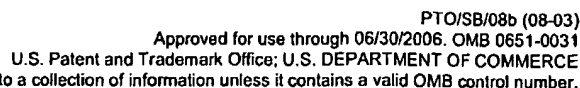
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	1
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Complete if Known

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Garrett
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

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**Examiner
Signature**

Kevin Verhegg

Date
Considered

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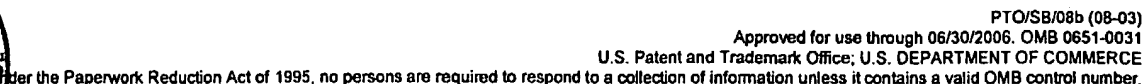
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PTO/SB/08a (08-03)



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Sheet	1	of	1
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Complete if Known

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Garrett
Art Unit	2818
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US1

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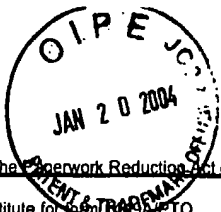
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 4

Complete if Known

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
KV		US-5,953,215	09-14-1999	Karabatsos	
		US-6,142,830	11-07-2000	Looeffler	
		US-6,185,644 B1	02-06-2001	Farmwald et al.	
		US-6,266,252 B1	07-24-2001	Karabatsos	
		US-6,317,352 B1	11-13-2001	Halbert et al.	
		US-6,414,904 B2	07-02-2002	So et al.	
		US-6,446,158 B1	09-03-2002	Karabatsos	
		US-6,493,250 B2	12-10-2002	Halbert et al.	
		US-6,530,006 B1	03-04-2003	Dodd et al.	
		US-6,643,752 B1	11-04-2003	Donnelly et al.	
		US-6,449,679 B2*	09-10-2002	Ryan	
		US-6,108,731*	08-22-2000	Suzuki et al.	
		US-5,701,313*	12-23-1997	Purdham	
		US-5,068,650*	11-26-1991	Fernandez et al.	
		US-4,947,257*	08-07-1990	Fernandez et al.	
		US-5,513,135*	04-30-1996	Dell et al.	
		US-6,065,092*	05-16-2000	Roy	
		US-6,185,644*	02-06-2001	Farmwald et al.	duplicate

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number * Kind Code ⁵ (if known)				

Examiner Signature	<i>Kevin Verhegg</i>	Date Considered	7/20/04
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2

of 4

Complete if Known

Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

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		Number-Kind Code ² (if known)			
KW		US-5,034,917*	07-23-1991	Bland et al.	
		US-4,977,498*	12-11-1990	Rastegar et al.	
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Kevin Verheij

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Sheet 3 of 4

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Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
KW		Intel Developer Forum, "DDR2 Memory In 2004 Servers - Recipes For Successful Designs", September 2003	
KW		QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
KW		KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
KW		Quad Band Memory (QBM), "The 'QBM Enabled' VIA PT880/PM880 Chipset Solutions with the Fastest Memory",	
KW		WEBER, F., "The AMD Athlon Processor: Future Directions", AMD	
KW		"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
KW		"Intel 82805AA Memory Translator Hub (MTH) Datasheet", Intel Corp., pp. 1-48 (Nov. 1999)	
KW		*CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
KW		*MAC WILLIAMS, P., "PC Memory Directions For 1999 and Beyond", Intel Developer Forum, pp. 1-10 (Sep. 1998)	
KW		*JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

Examiner
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Application Number	10/625,276
Filing Date	July 23, 2003
First Named Inventor	Richard E. Perego
Art Unit	
Examiner Name	
Attorney Docket Number	RAMB-01049US1

NON PATENT LITERATURE DOCUMENTS

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KW		*GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SLDRAM Consortium, SLDRAM Inc., pp. 1-14 (Aug. 1997)	
KW		**Draft Standard for a High-Speed Memory Interface (Synclink)", Draft 0.99 IEEE P1596.7-199X, IEEE Inc., pp. 1-51 (1996)	
KW		*WIGGERS, H., "SyncLink A Proposal for an Implementation of IEEE P1596.4", Hewlett Packard, pp. 1-20 (Mar. 1995)	
KW		**KMM377S1620CT2 SDRAM Module Datasheet", Samsung Electronics Inc., pp. 1-2 (Nov. 1998)	
KW		**SLD4M18DR400 4MEG x 18 SLDRAM Datasheet", SLDRAM Inc., pp. 1-69 (July 1998)	
KW		*POULTON, J., "Signaling in High Performance Memory Systems", IEEE Solid State Circuits Conference, slides 1-59 (Feb. 1999)	
KW		**SLDRAM 400Mb/s/pin Command/Address Repeater", SLD10400 Rev .5, SLDRAM Consortium, pp. 1-12 (Jan. 7, 1998)	
KW		*Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "SyncLink", May 24, 1995, New Orleans	

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